

Lab X Audio IP for Xilinx AVB



- AVB IEEE Standards based, royalty free AV networking
- Xilinx FPGAs provide design flexibility, system integration, and upgrade capability
- Lab X IP can be incorporated into an unlimited number of products

Pricing for Lab X Audio Packetizer (Input Packet Manager, IPM), Depacketizer (Output Packet Manager, OPM), Media Clock Recovery (MCR), Stream Reservation Protocol (SRP), Lab X Optimized 10/100/1000 MAC + Lab X “Endpoint” for Xilinx FPGAs

Provide Functionality for

- IEEE 802.1Qav
- IEEE 802.1Qat
- IEEE 802.1AS
- IEEE 1722
- IEEE 1722.1

	Site License		
Details	Netlisted component License to a 5 mile radius of a specific geographic site		
Price	One time Site License fee (details available under NDA) + (optional) Annual Maintenance		
Annual Maintenance	Cummulative Units	Annual Optional Maintenance	Annual Hours of Support ¹
	1-9,999	10% of original Site License	50 Hours
	10,000 – 49,999	15% of original Site License	75 Hours
	50,000 – 99,999	20% of original Site License	100 Hours
	100,000+	25% of original Site License	125 Hours
	<ul style="list-style-type: none"> • Most cost effective “long term” solution, which can be incorporated into an unlimited number of products • No per channel or per unit royalties • Maintenance keeps solution up to date with latest standards and Xilinx device platforms 		

1. Maintenance hours renew annually with maintenance payment, and are valid for a twelve month period and do not accrue. Support hours exclude customization and cannot be redeemed for design services.

Audio AVB for Xilinx model – January 3, 2011

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